



## FIRST INTERNATIONAL NANOTECHNOLOGY CONFERENCE ON COMMUNICATION AND COOPERATION

### Speaker's Bio

#### Toshiro Hiramoto



Toshiro Hiramoto received B.S., M.S., and Ph.D degrees in electronic engineering from the University of Tokyo in 1984, 1986, and 1989, respectively. In 1989, he joined Device Development Center, Hitachi Ltd., Ome, Japan, where he was engaged in the device and circuit design of ultra-fast BiCMOS SRAMs. In 1994, he joined Institute of Industrial Science, University of Tokyo, Japan, as an Associate Professor. He was also an Associate Professor in VLSI Design and Education Center, University of Tokyo, from 1996 to 2002. He has been a Professor in Institute of Industrial Science, University of Tokyo since 2002. His research interests include low power and low voltage design of advanced

CMOS devices, SOI MOSFETs, device/circuit cooperation scheme for low power VLSI, quantum effects in nano-scale MOSFETs, and silicon single electron transistors.

Dr. Hiramoto is a member of IEEE, IEICE, and Japan Society of Applied Physics. He has been an Elected AdCom Member of IEEE Electron Devices Society since 2001. He served as the General Chair of Silicon Nanoelectronics Workshop in 2003, and the Program Chair in 1997, 1999, and 2001. He also served on Program Subcommittee on Integrated Circuits of IEEE International Electron Devices Meeting (IEDM) in 1993 and 1994 and on Program Subcommittee on CMOS Devices of IEDM in 2003 and 2004, and has served on Program Committee of Symposium on VLSI Technology since 2001. He is the Subcommittee Chair of CMOS Devices of IEDM in 2005.